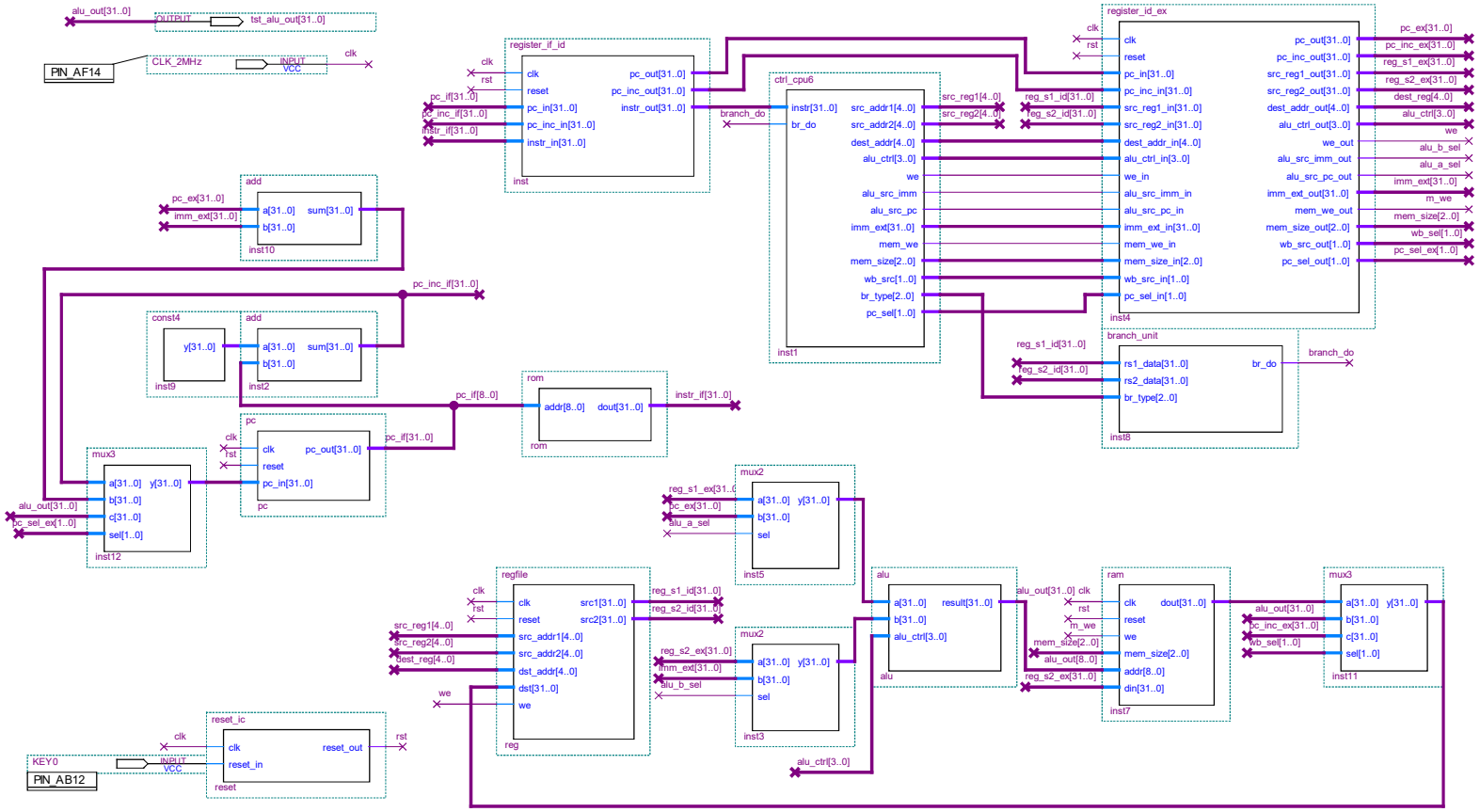
alu_out[31..0]
OUTPUT
tst_alu_out[31..0]
PIN_AF14
CLK_2MHz
INPUT
VCC
clk
register_if_id
pc_out[31..0]
pc_inc_out[31..0]
instr_out[31..0]
pc_if[31..0]
pc_inc_if[31..0]
instr_if[31..0]
inst
ctrl_cpu6
instr[31..0]
br_do
src_addr1[4..0]
src_addr2[4..0]
dest_addr[4..0]
alu_ctrl[3..0]
we
alu_src_imm
alu_src_pc
imm_ext[31..0]
mem_we
mem_size[2..0]
wb_src[1..0]
br_type[2..0]
pc_sel[1..0]
inst1
src_reg1[4..0]
src_reg2[4..0]
reg_s1_id[31..0]
reg_s2_id[31..0]
branch_do
register_id_ex
pc_in[31..0]
pc_inc_in[31..0]
src_reg1_in[31..0]
src_reg2_in[31..0]
dest_addr_in[4..0]
alu_ctrl_in[3..0]
we_in
alu_src_imm_in
alu_src_pc_in
imm_ext_in[31..0]
mem_we_in
mem_size_in[2..0]
wb_src_in[1..0]
pc_sel_in[1..0]
pc_out[31..0]
pc_inc_out[31..0]
src_reg1_out[31..0]
src_reg2_out[31..0]
dest_addr_out[4..0]
alu_ctrl_out[3..0]
we_out
alu_src_imm_out
alu_src_pc_out
imm_ext_out[31..0]
mem_we_out
mem_size_out[2..0]
wb_src_out[1..0]
pc_sel_out[1..0]
pc_ex[31..0]
pc_inc_ex[31..0]
reg_s1_ex[31..0]
reg_s2_ex[31..0]
dest_reg[4..0]
alu_ctrl[3..0]
we
alu_b_sel
alu_a_sel
imm_ext[31..0]
m_we
mem_size[2..0]
wb_sel[1..0]
pc_sel_ex[1..0]
inst4
add
pc_ex[31..0]
imm_ext[31..0]
a[31..0]
b[31..0]
sum[31..0]
inst10
const4
y[31..0]
inst9
inst2
rom
addr[8..0]
dout[31..0]
pc_if[8..0]
instr_if[31..0]
pc
clk
rst
reset
pc_out[31..0]
pc_in[31..0]
pc_if[31..0]
mux3
a[31..0]
b[31..0]
c[31..0]
sel[1..0]
y[31..0]
alu_out[31..0]
pc_sel_ex[1..0]
inst12
branch_unit
rs1_data[31..0]
rs2_data[31..0]
br_type[2..0]
br_do
reg_s1_id[31..0]
reg_s2_id[31..0]
branch_do
inst8
mux2
reg_s1_ex[31..0]
pc_ex[31..0]
alu_a_sel
inst5
regfile
src1[31..0]
src2[31..0]
src_addr1[4..0]
src_addr2[4..0]
dst_addr[4..0]
dst[31..0]
we
reg_s1_id[31..0]
reg_s2_id[31..0]
src_reg1[4..0]
src_reg2[4..0]
dest_reg[4..0]
reg
reg_s2_ex[31..0]
imm_ext[31..0]
alu_b_sel
inst3
alu
a[31..0]
b[31..0]
alu_ctrl[3..0]
result[31..0]
alu_out[31..0]
alu_ctrl[3..0]
ram
dout[31..0]
reset
we
mem_size[2..0]
addr[8..0]
din[31..0]
alu_out[8..0]
reg_s2_ex[31..0]
inst7
mux3
alu_out[31..0]
pc_inc_ex[31..0]
wb_sel[1..0]
inst11
KEY0
PIN_AB12
reset_ic
reset_in
reset_out
reset